

ABSTRACT

Systems, methodologies, media, and other embodiments associated with detecting a cache coherency protocol mode associated with a system that provided a point-to-point transaction to a virtual bus interface are described. One exemplary system embodiment includes a detection logic that facilitates identifying the cache coherency protocol mode. The exemplary system embodiment may also include a coding logic that facilitates selectively processing packets associated with a point-to-point transaction into a bus model transaction, where the packet processing depends, at least in part, on the cache coherency protocol mode.

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